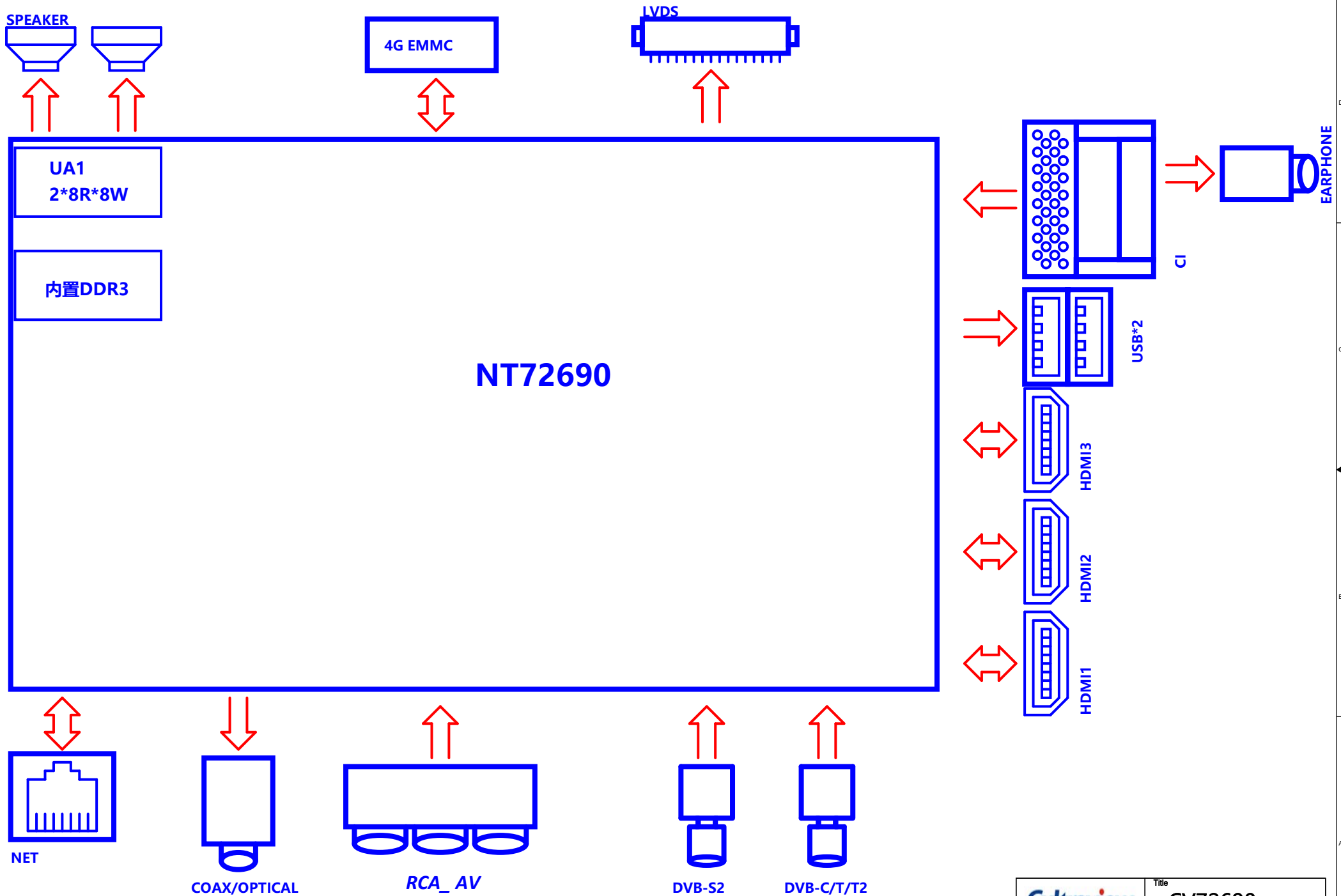
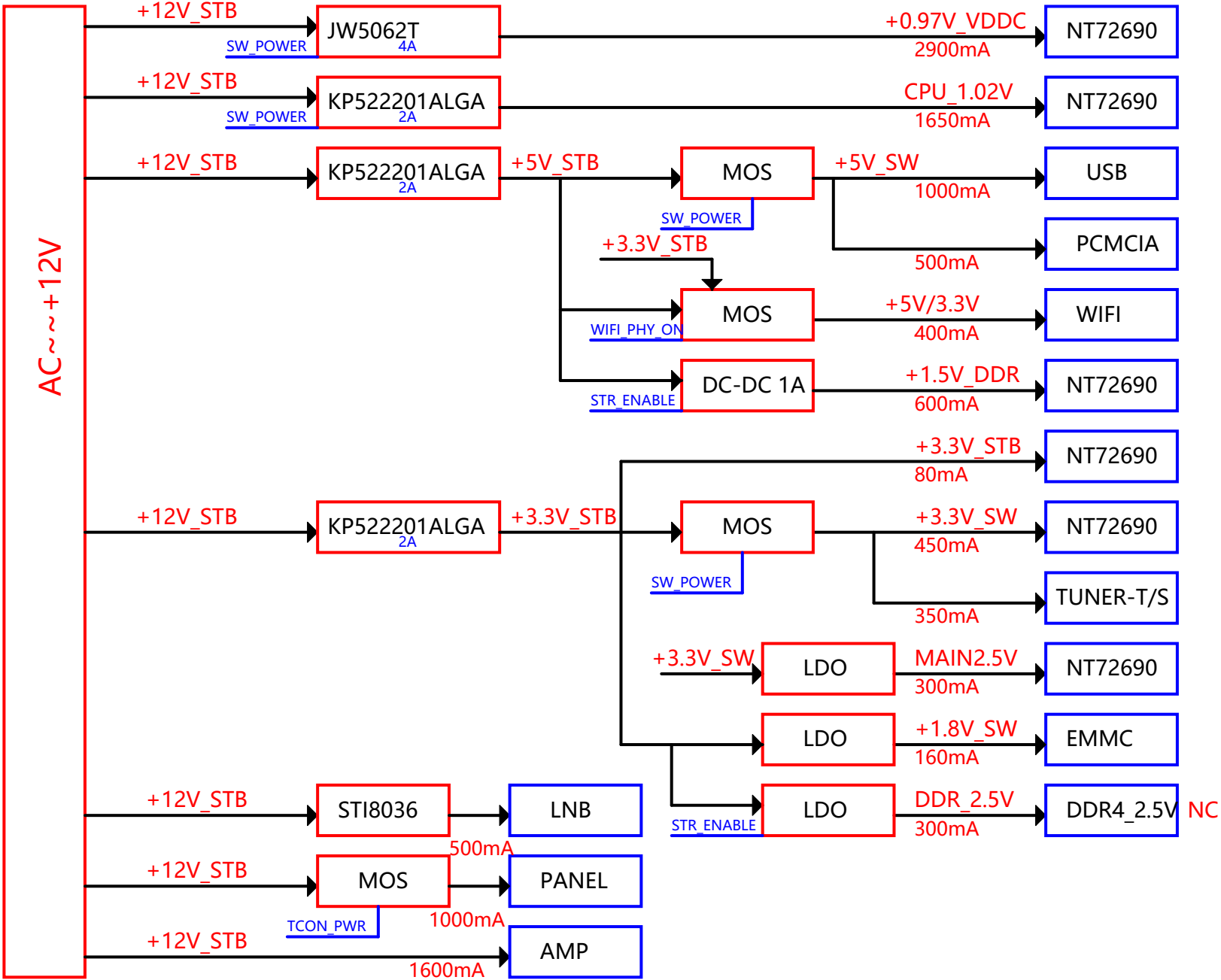
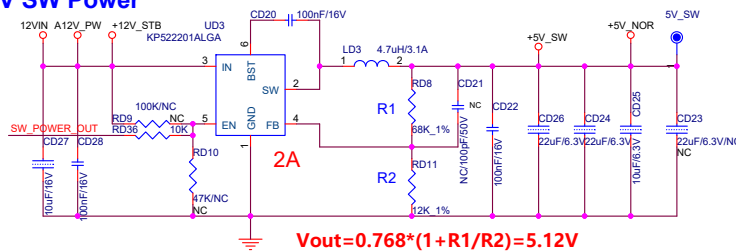




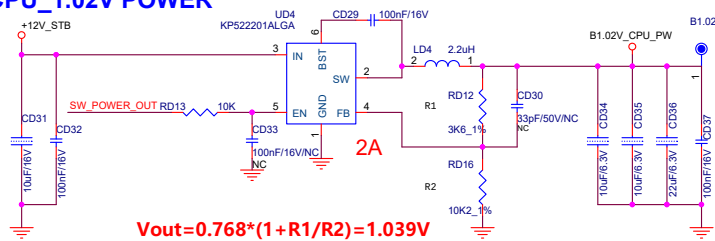
POWER SUPPLY ARCHITECTURE



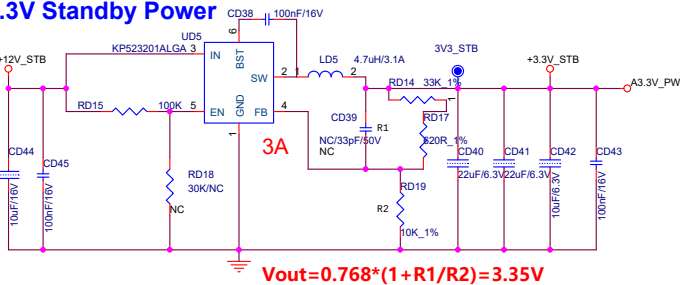
5V SW Power



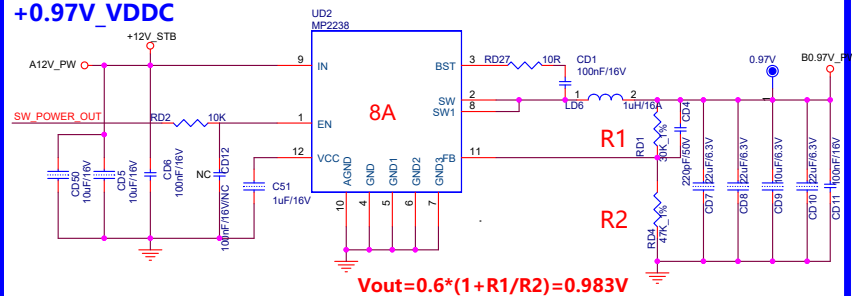
CPU_1.02V POWER



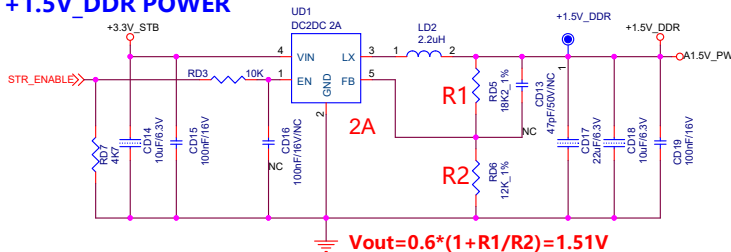
3.3V Standby Power



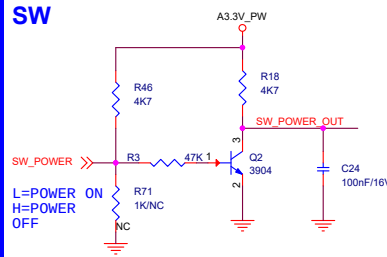
+0.97V_VDDC



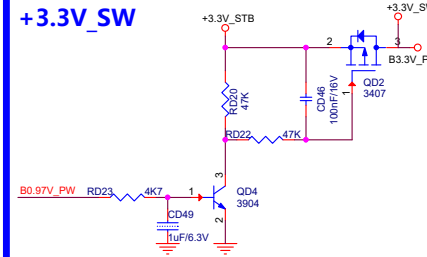
+1.5V_DDR POWER



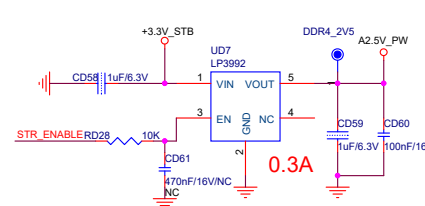
SW



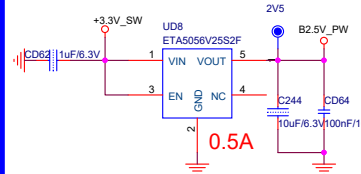
+3.3V_SW



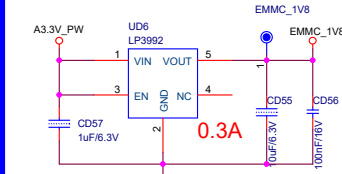
DDR4_2.5V



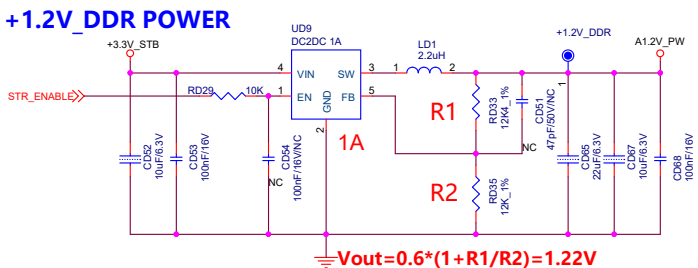
MAIN_2.5V



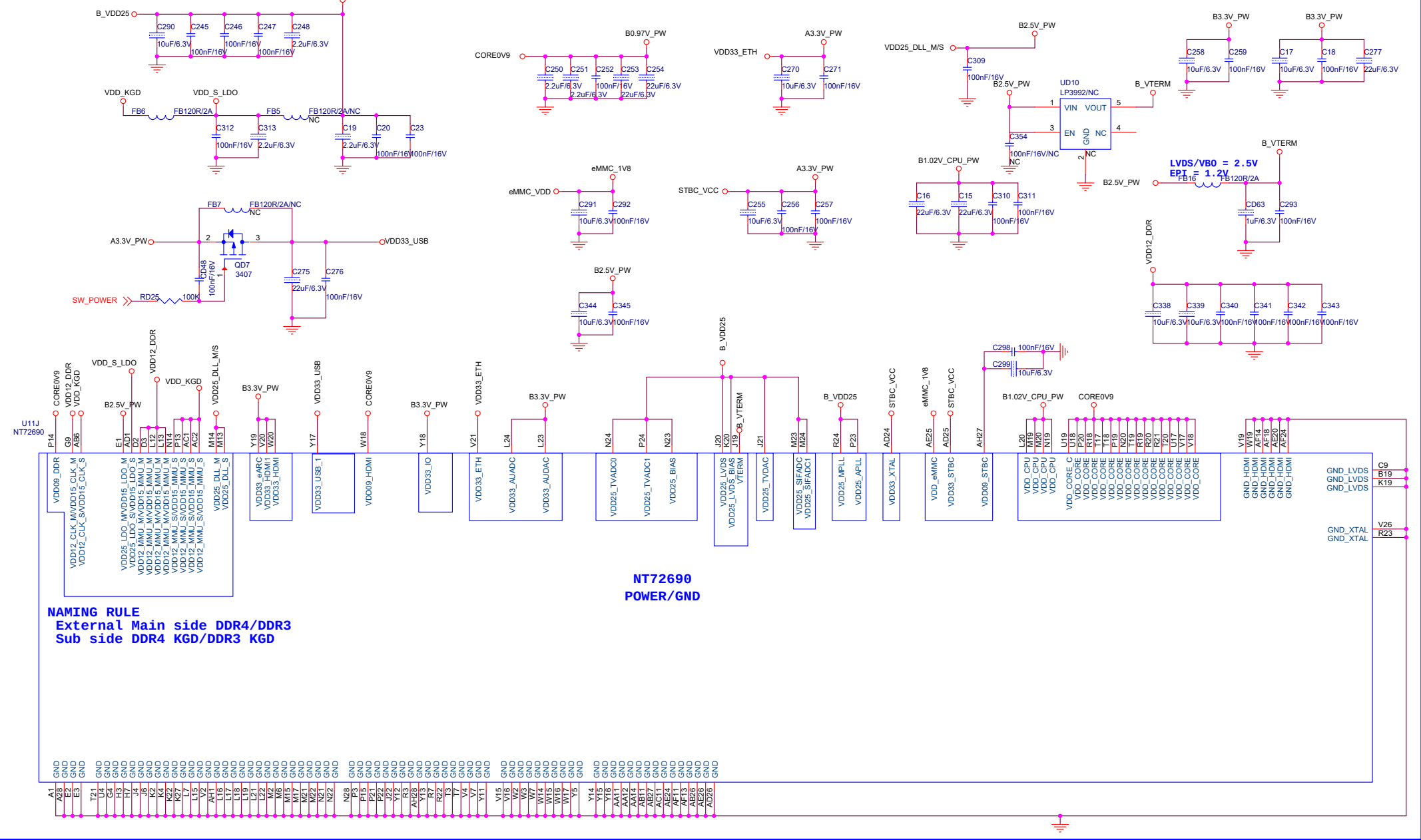
EMMC 1.8V

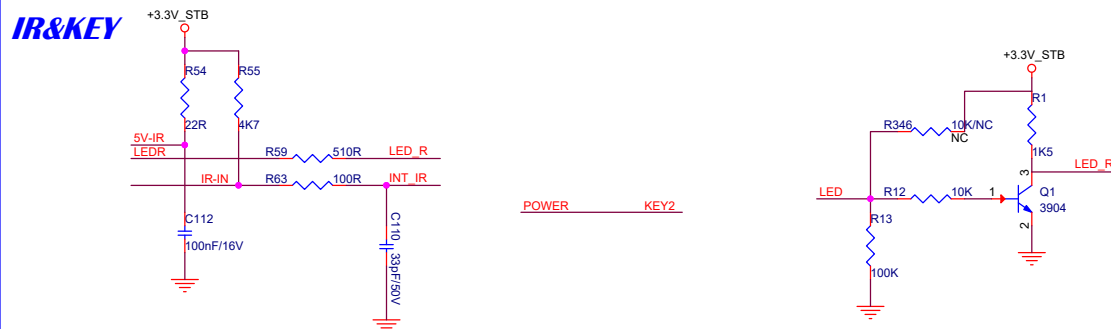
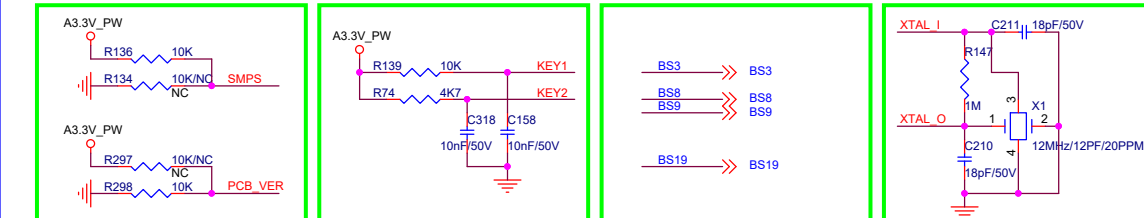


+1.2V_DDR POWER

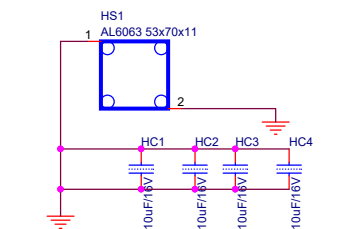
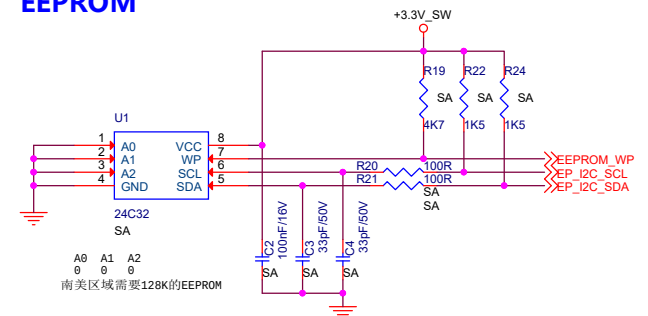


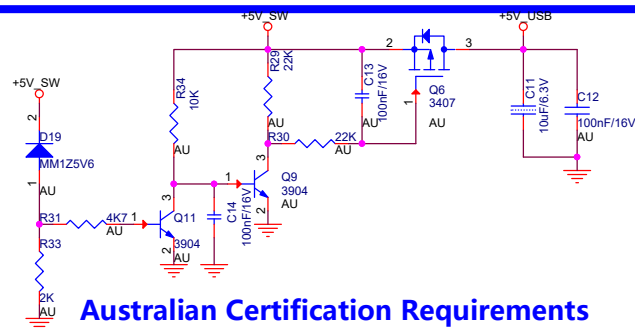
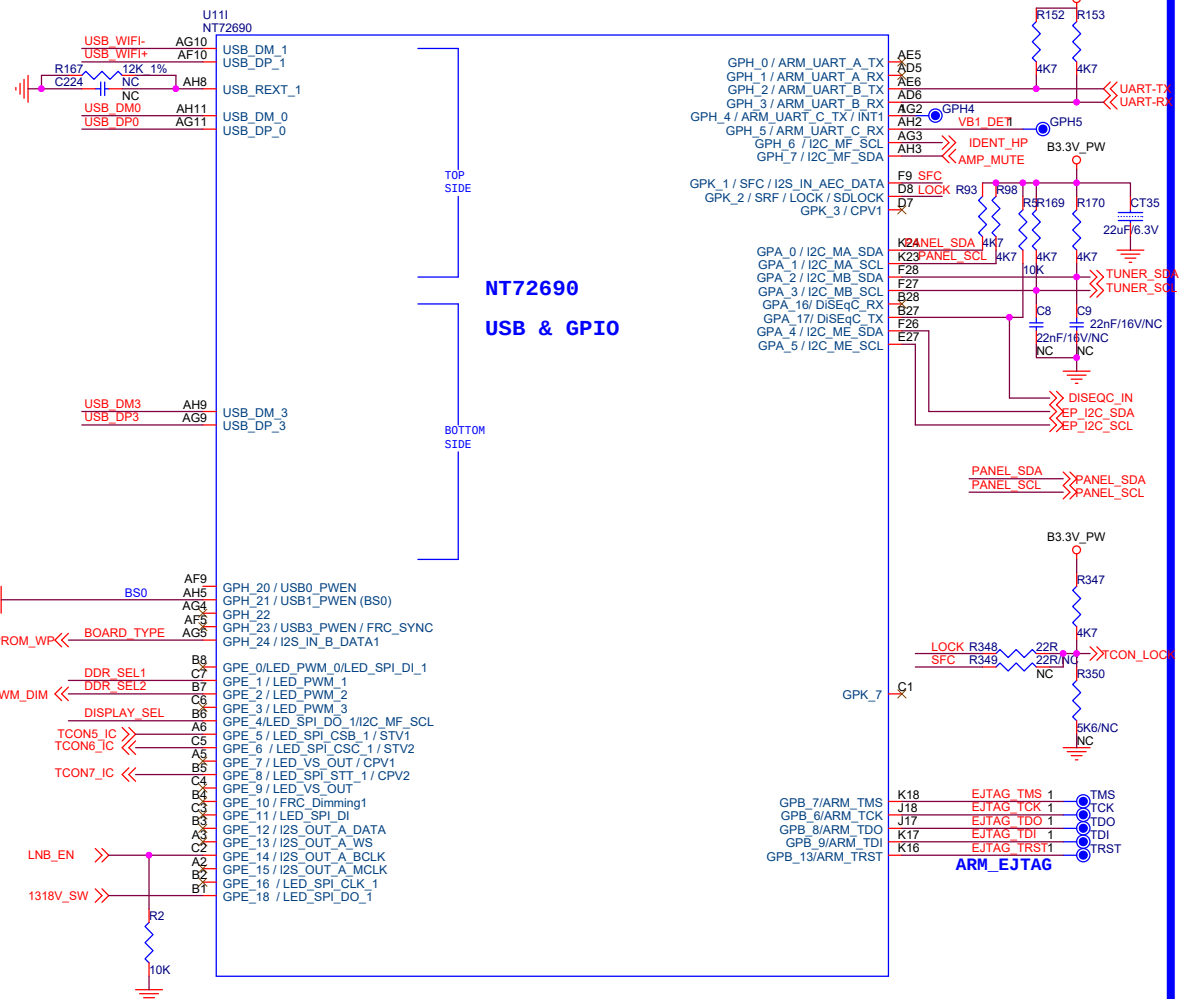
IC POWER&GND



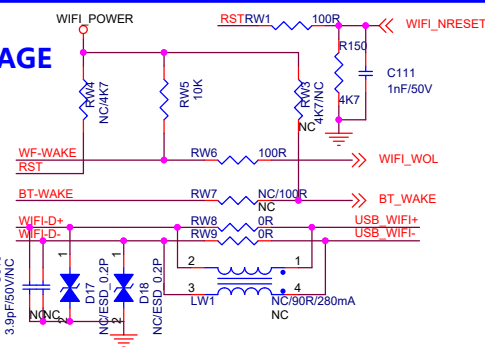


Heat Sink

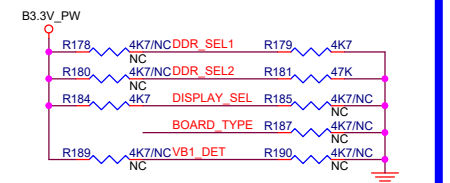
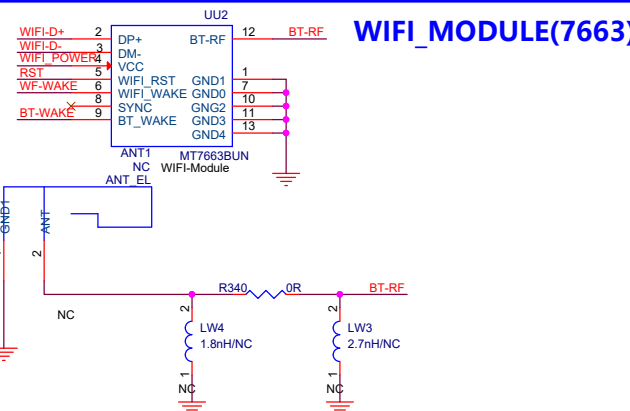
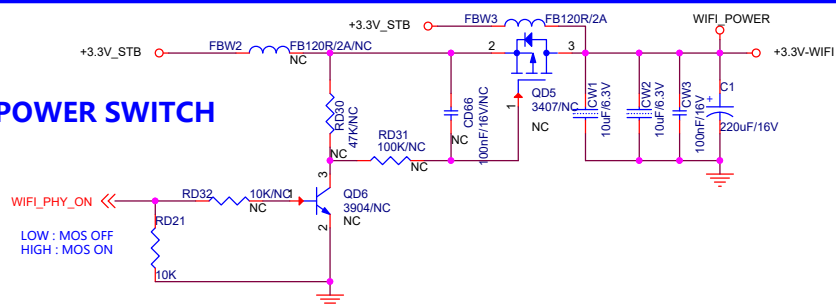


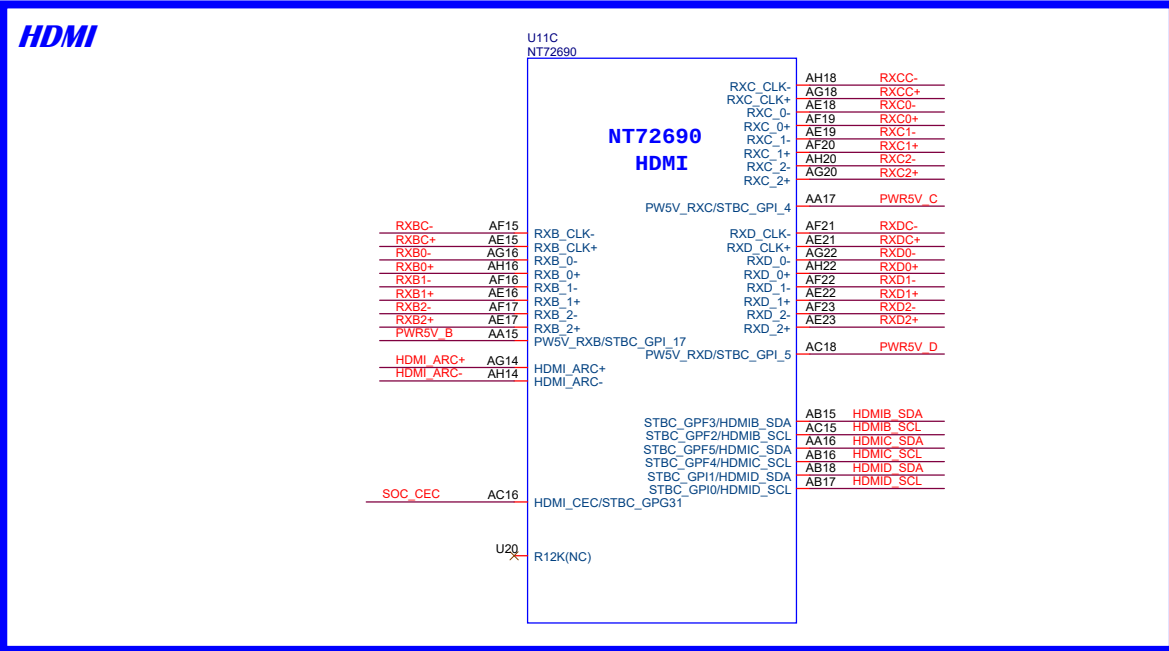
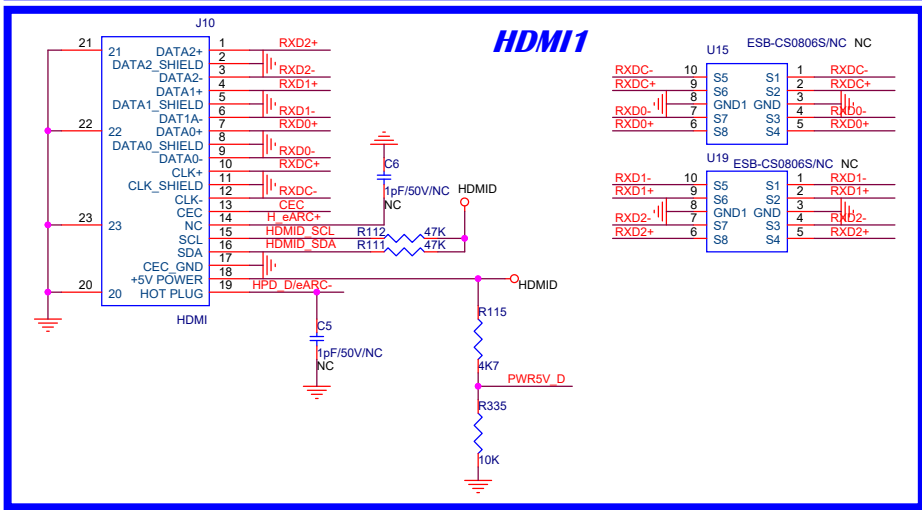
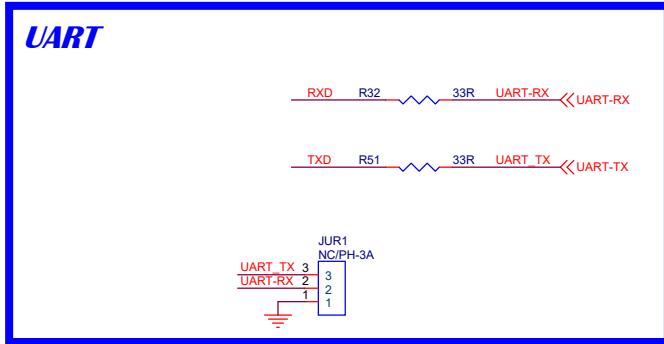
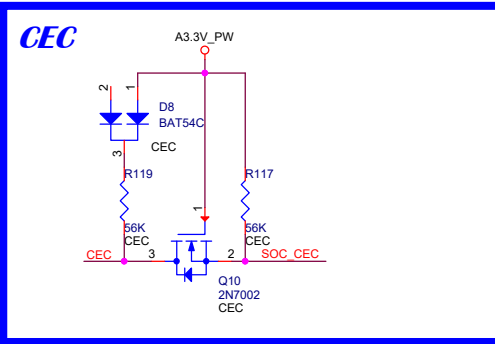
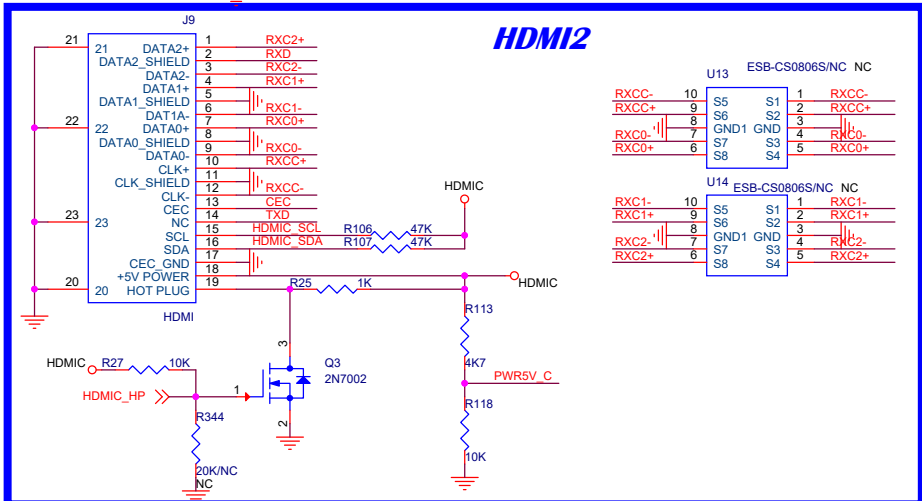
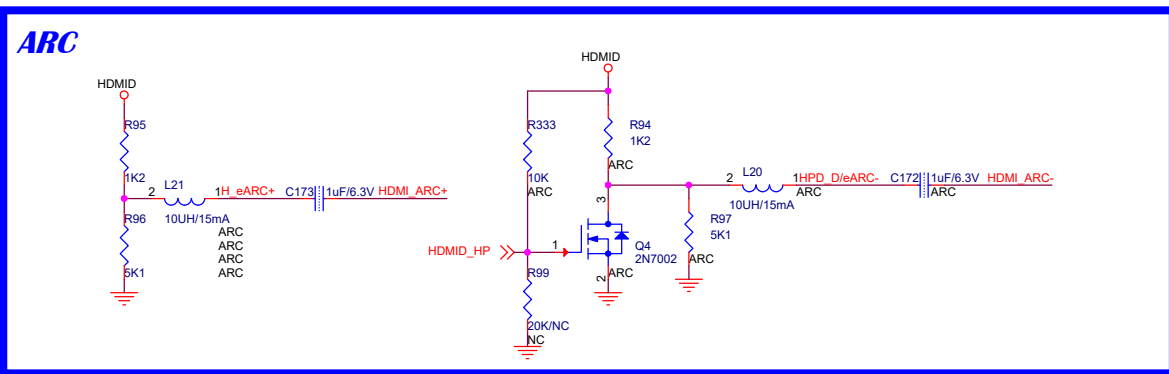
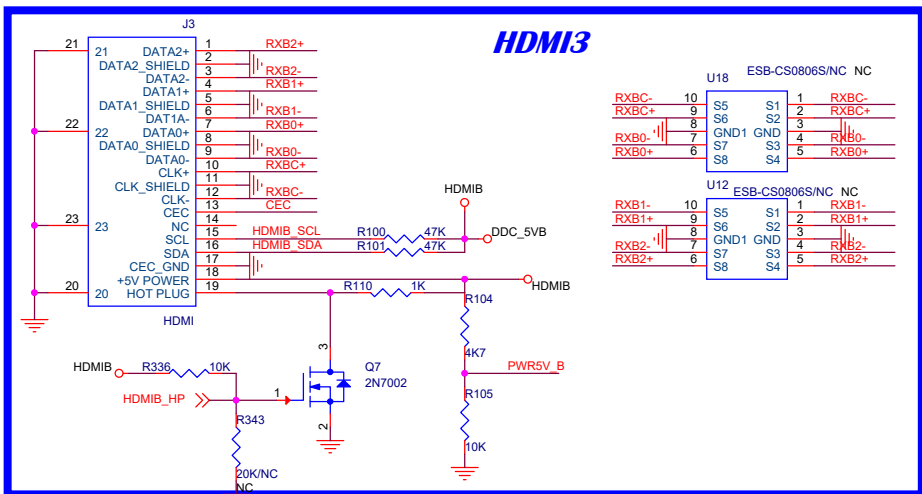


SIGNAL OFFPAGE

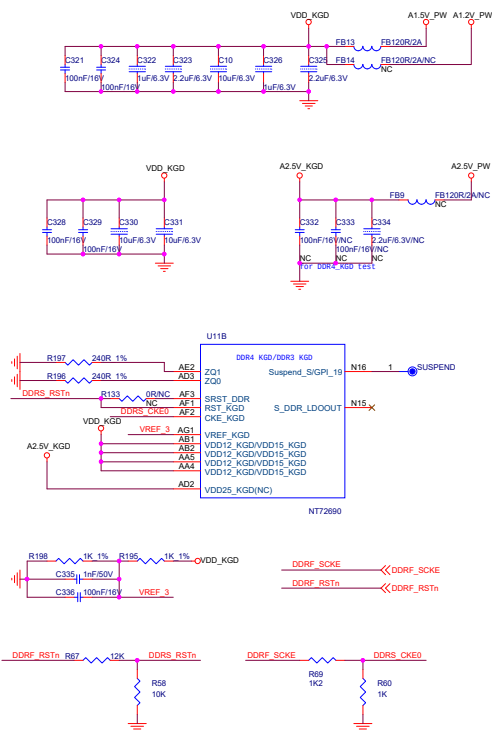


POWER SWITCH

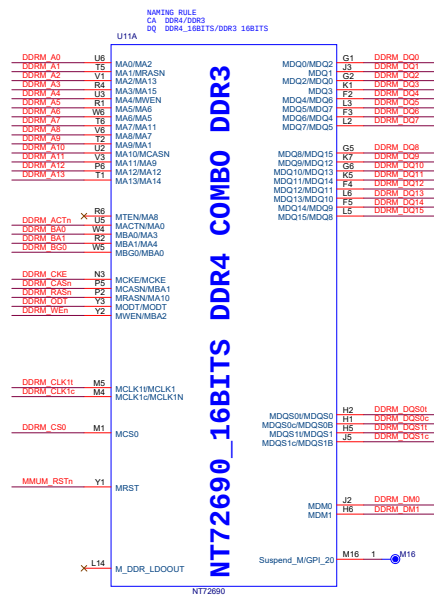
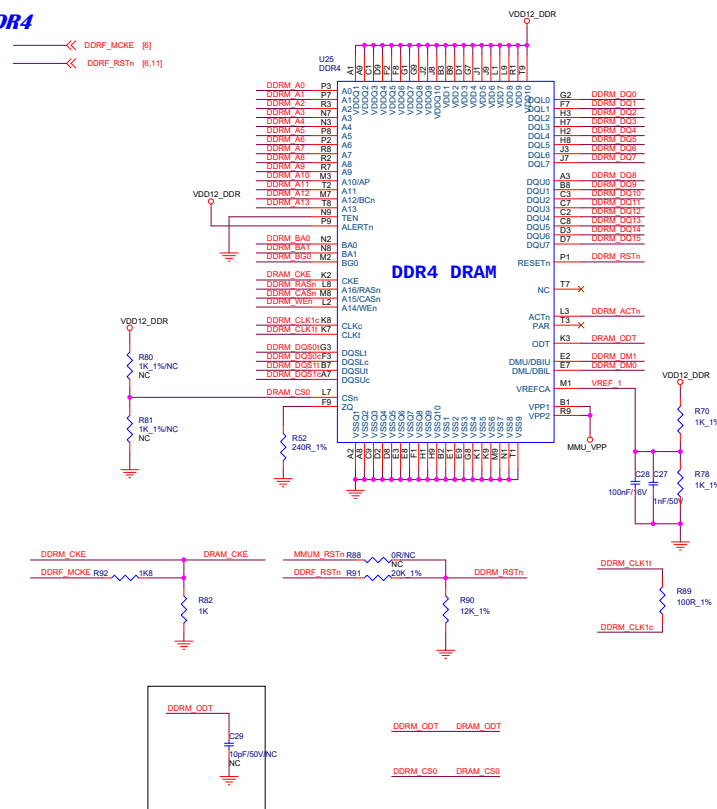
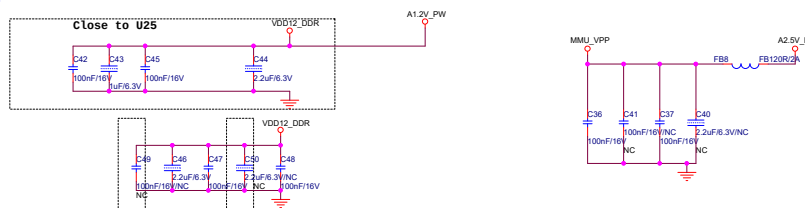


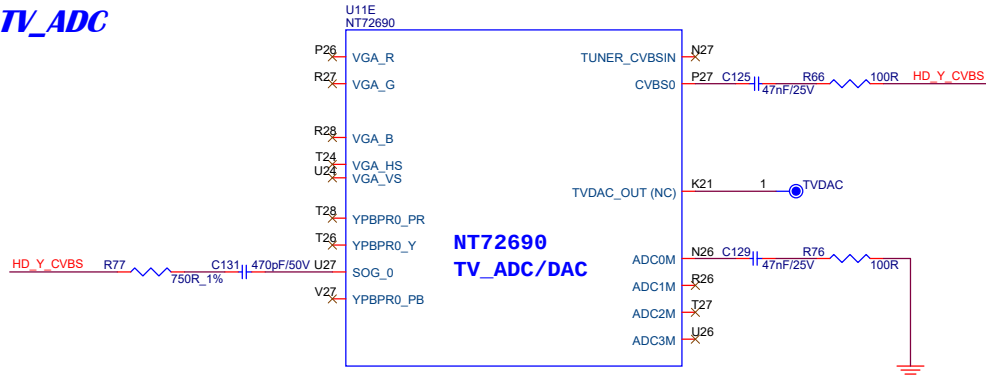
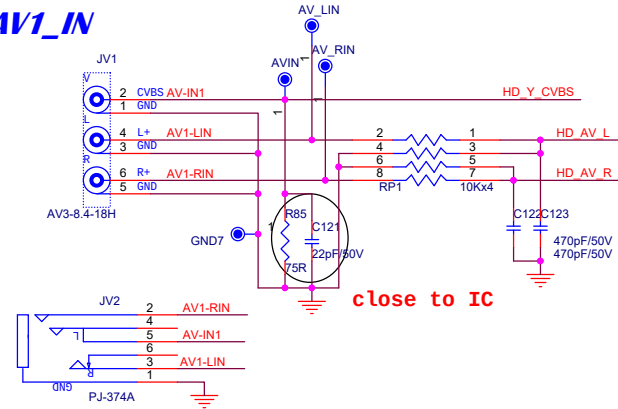
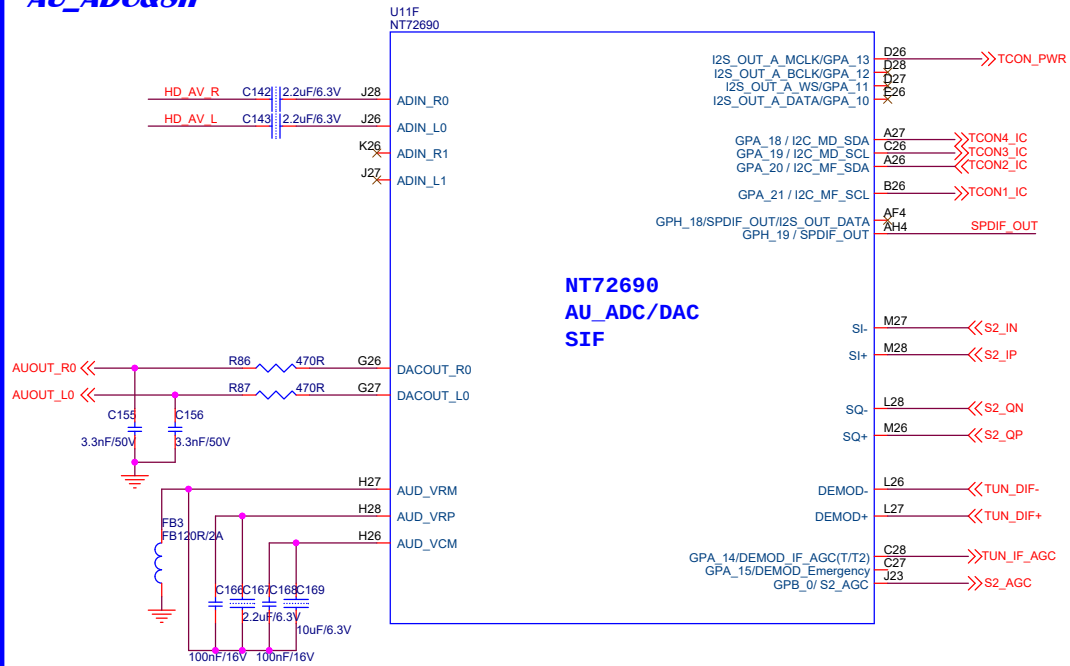


DDR_S

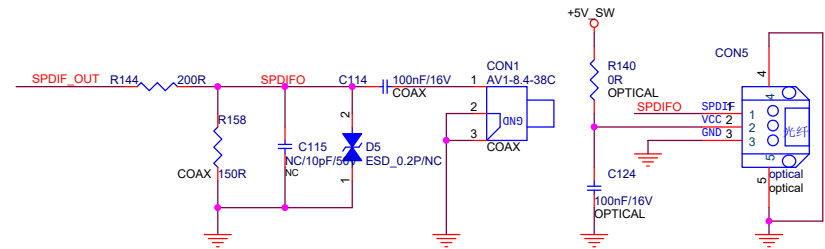


DDR_M

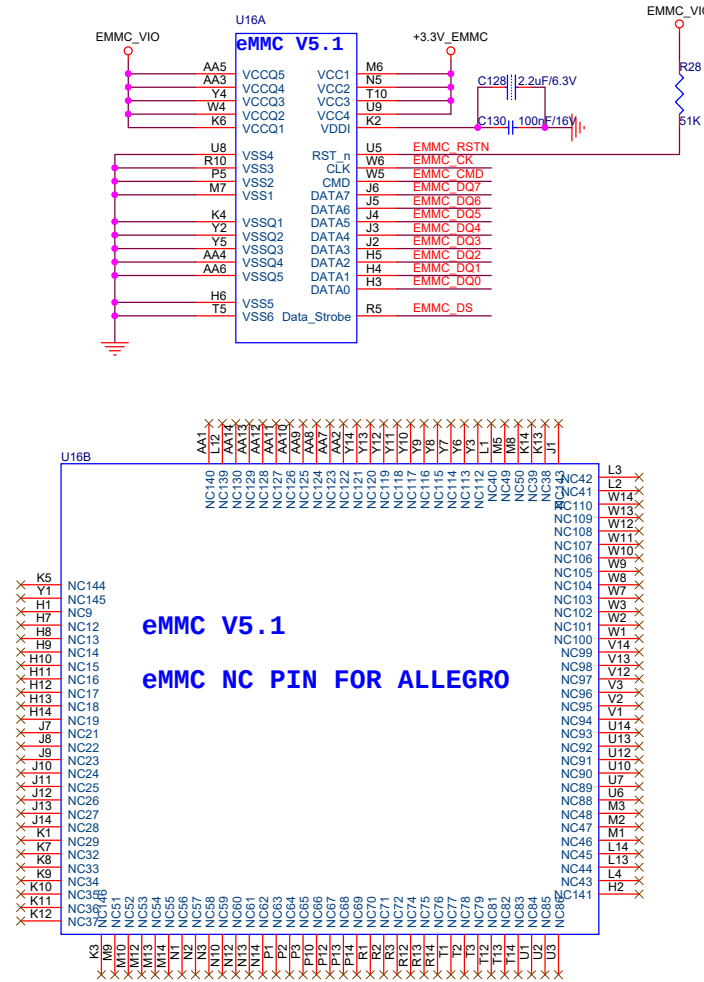
**DDR4****DDR4_POWER**

TV_ADC**AV1_IN*****AU_ADC&SIF***

SPDIF OUT

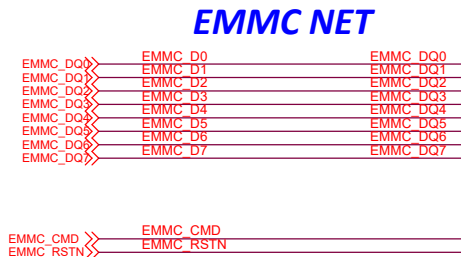
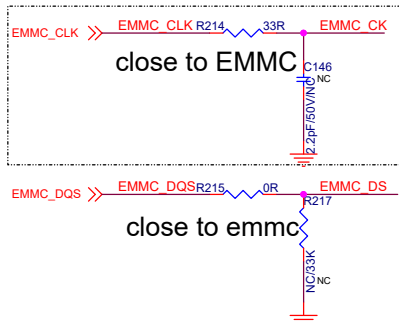


EMMC



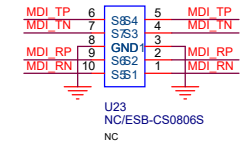
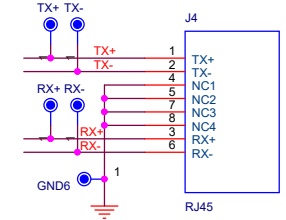
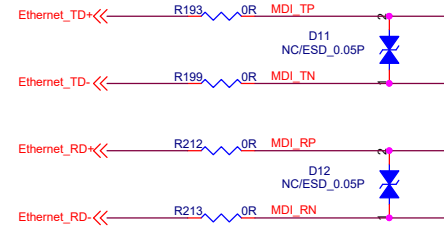
EMMC V5.1

EMMC NC PIN FOR ALLEGRO

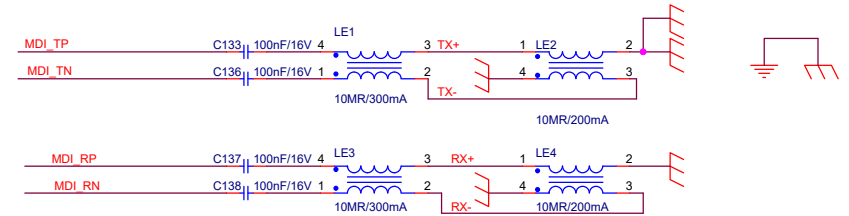


EMMC NET

Ethernet Connetor



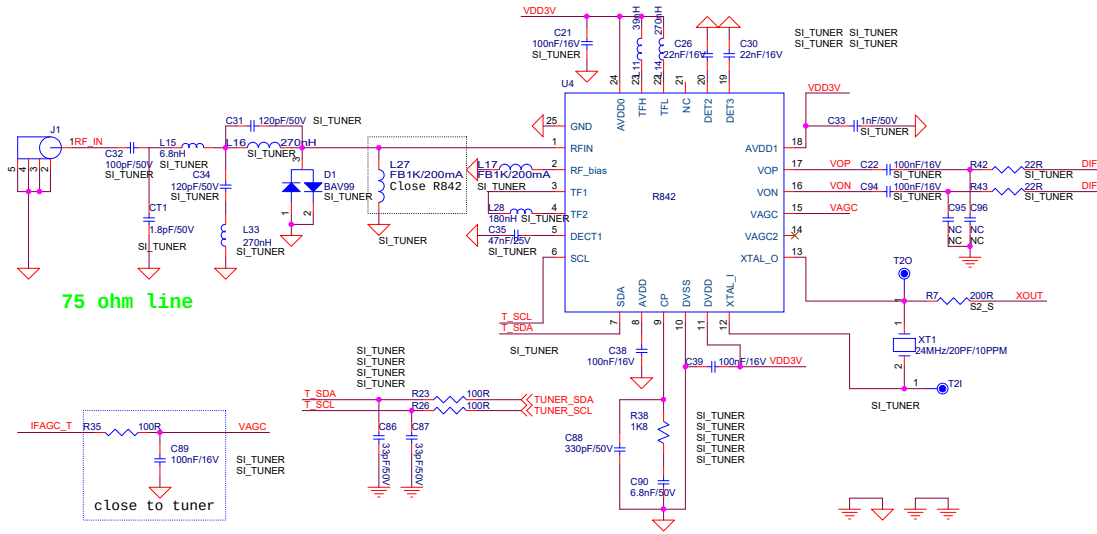
ENTHNET 分立



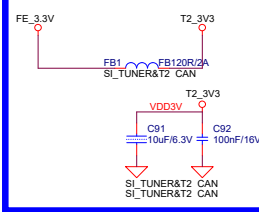
EMMC Power



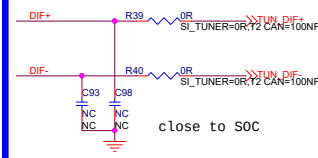
T/T2/C TUNER



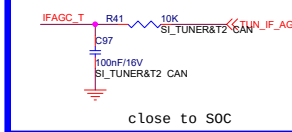
T2 Tuner Power



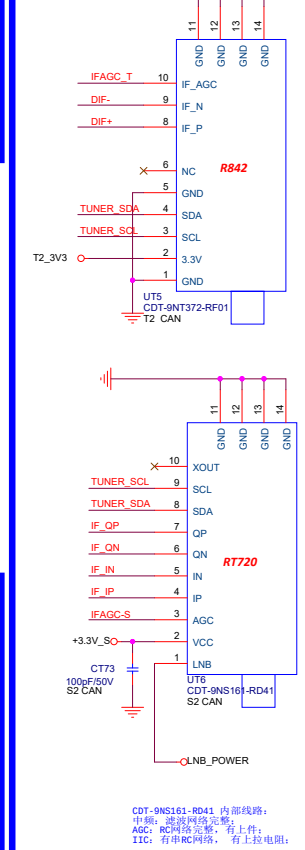
T2_IF



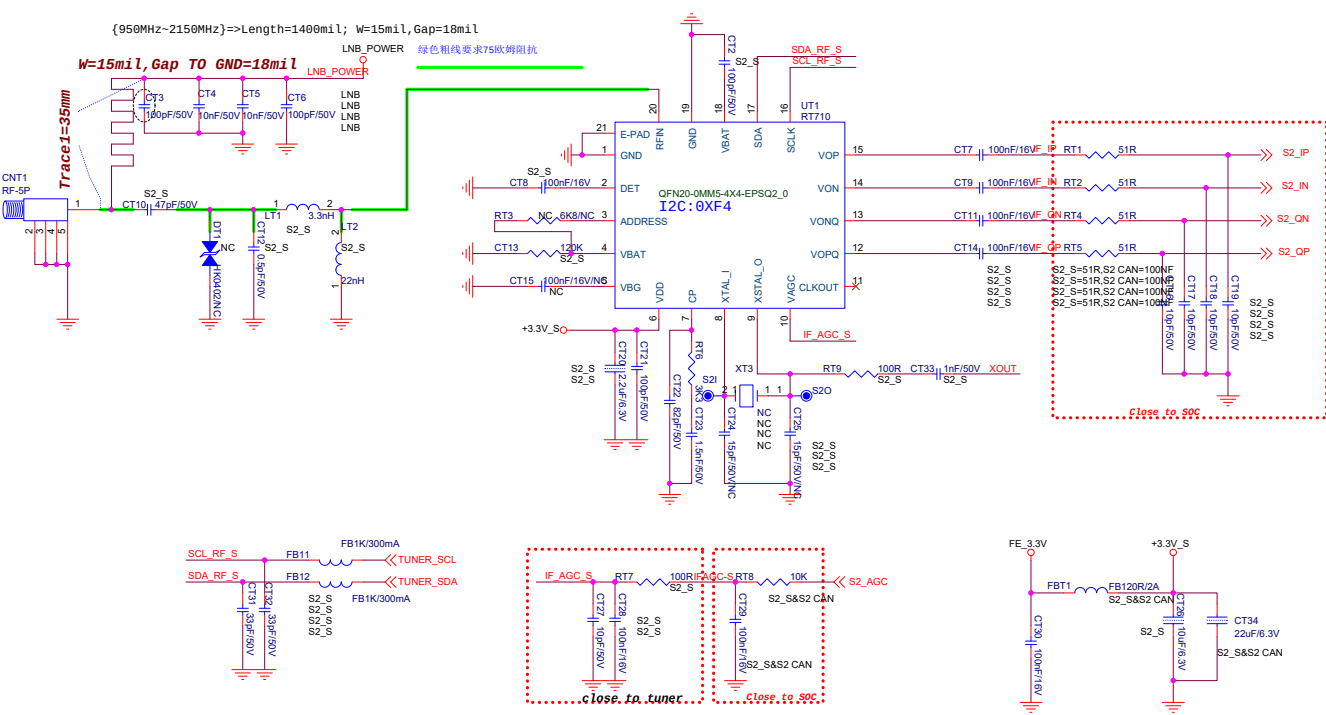
T2_AGC



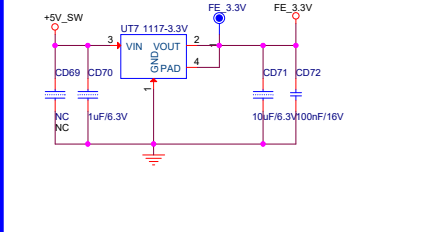
CAN TUNER



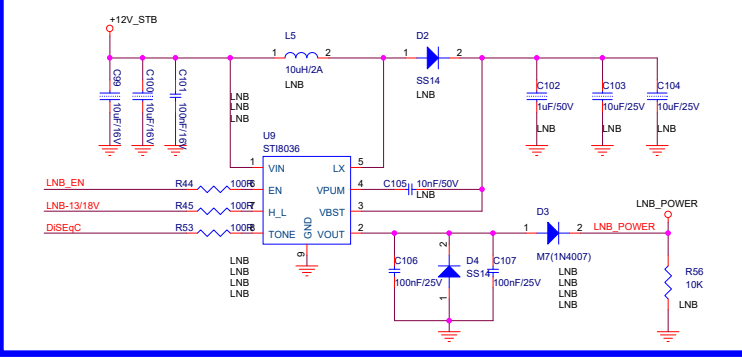
S2 TUNER



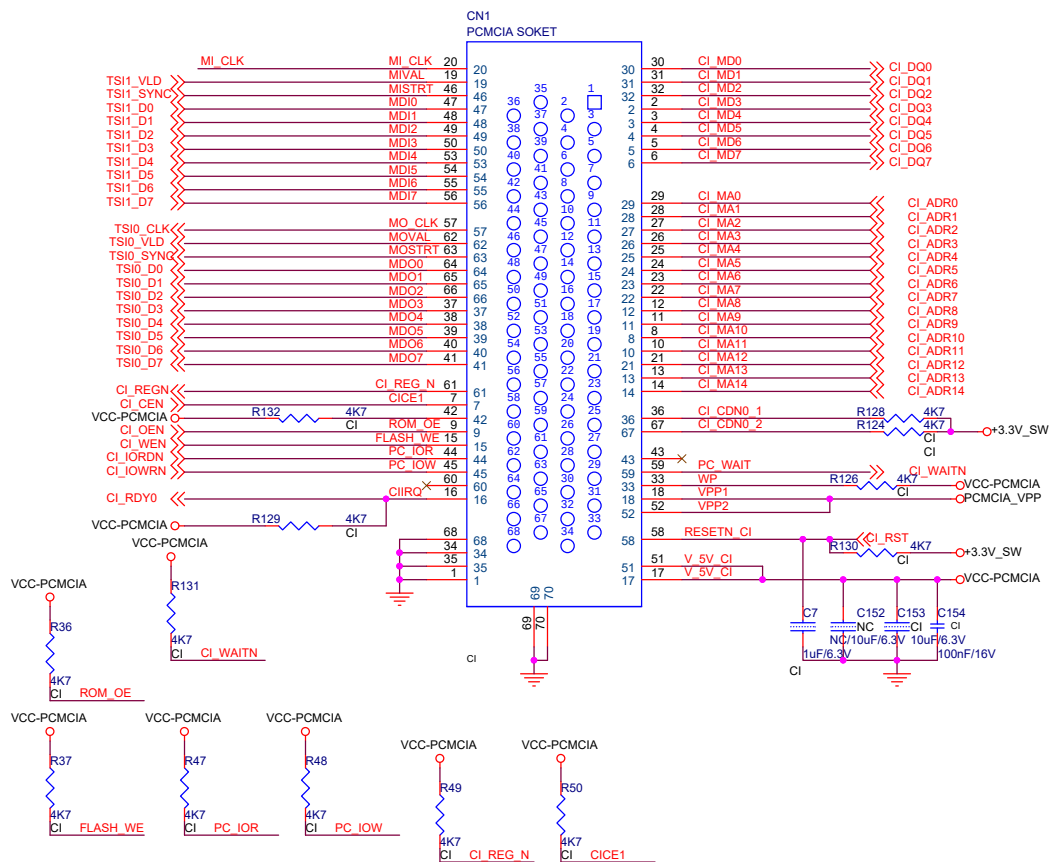
Tuner Power



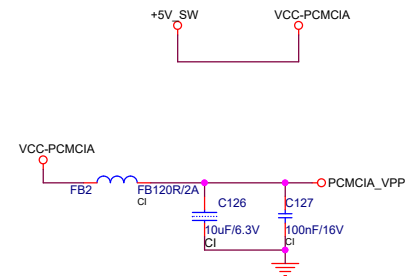
LNB POWER



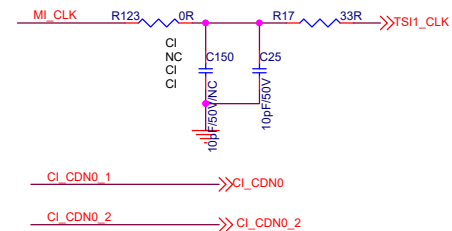
CI Socket



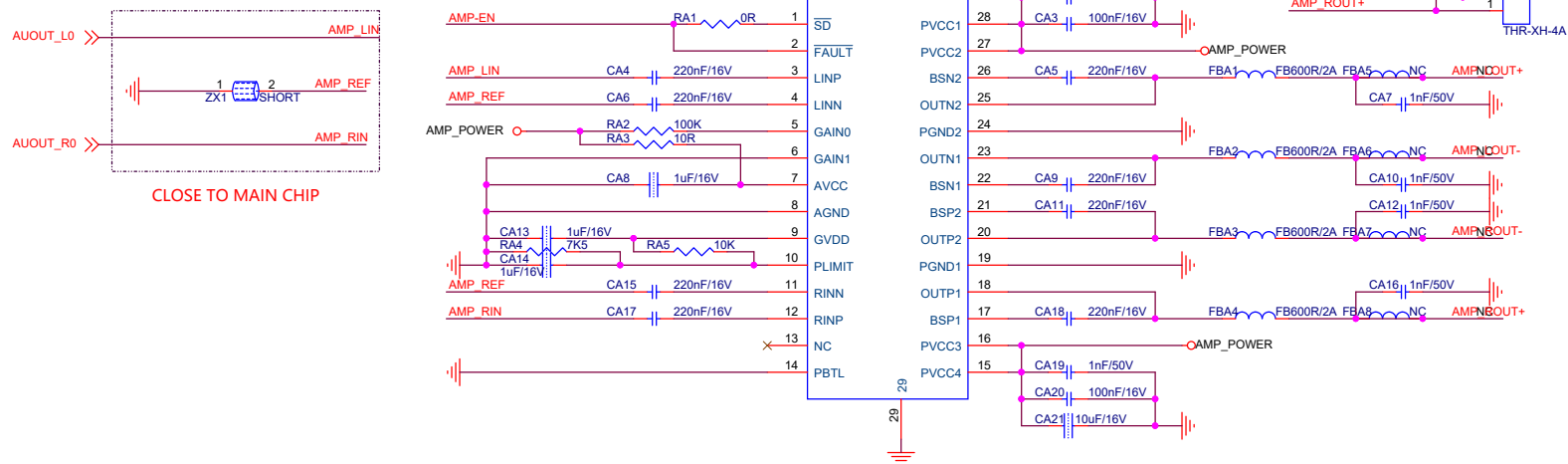
PCMCIA POWER



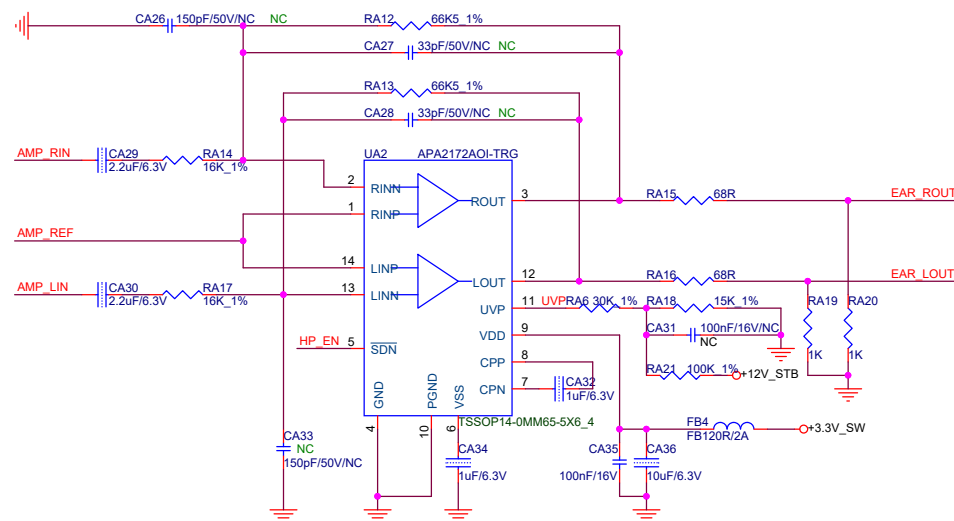
CLK



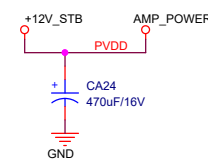
AMPLIFY



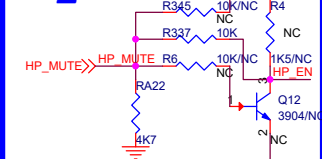
Headphone OP



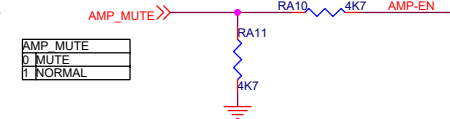
POWER



HP_MUTE



MUTE



EARPHONE

